

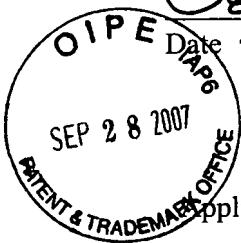
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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

September 25, 2007 *Alexandra Beggs Jr*

Alexandra Beggs



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul A. LaBerge Attorney Docket No.: 501128.01
Patent No. : US 6,898,648 B2 Serial No. : 10/081,652
Issue Date : May 24, 2005 Filed : February 21, 2002
Title : MEMORY BUS POLARITY INDICATOR SYSTEM AND METHOD FOR
REDUCING THE EFFECTS OF SIMULTANEOUS SWITCHING OUTPUTS (SSO)
ON MEMORY BUS TIMING

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate
OCT 02 2007
of Correction

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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**(SSO) ON MEMORY BUS
TIMING”**

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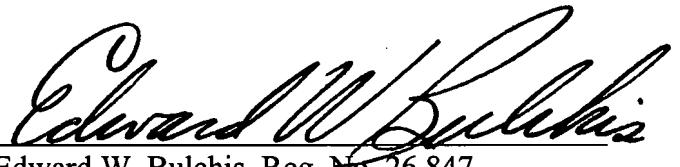
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Respectfully submitted,

Date: Sept. 24, 2007

By:



Edward W. Bulchis, Reg. No. 26,847
Customer No. 27,076
Dorsey & Whitney LLP
1420 Fifth Avenue, Suite 3400
Seattle, WA 98101
(206) 903-8785
Attorney for Applicant(s)

EWB:tdp

Enclosures:

Postcard
Form PTO-1050 (+ copy)

501128.01 req cert correct

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UNITED STATES PATENT AND TRADEMARK OFFICE

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invert"

invert--

MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101

Patent No. US 6,898,648 B2

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